o Andújar e cover sheet with the co AINS) CLOSED in this app		
er o Andújar e cover sheet with the co AINS) CLOSED in this app	2826 prespondence address /	
e cover sheet with the co AINS) CLOSED in this app	prrespondence address /	
AINS) CLOSED in this app		
	will be mailed in due course. THIS withdrawal from issue at the initiative	
<u>/2005</u> . Stipp	NATHAN J. FLANN	
TE:	RVISORY PATENT EXAMINER CHNOLOGY CENTER 2800	
eived. eived in Application No have been received in this remunication to file a reply of is application. e the attached EXAMINER's (s) why the oath or declarate	complying with the requirements S AMENDMENT or NOTICE OF	
	948) attached	
1) hereto or 2) to Paper No./Mail Date		
nent / Comment or in the O	ffice action of	
	igs in the front (not the back) of al).	
7. DEPOSIT OF and/or INFORMATION about the deposit of BIOLOGICAL MATERIAL must be submitted. Note the attached Examiner's comment regarding REQUIREMENT FOR THE DEPOSIT OF BIOLOGICAL MATERIAL.		
6. Interview Summary Paper No./Mail Dat 7. Examiner's Amendo	ė .	
. cch rh	S.C. § 119(a)-(d) or (f). ceived. ceived in Application No have been received in this r mmunication to file a reply on a separate of the separate of th	

Application/Control Number: 10/621,654

Art Unit: 2826

DETAILED ACTION

Page 2

Allowed Claims

1. Claims 1-11 are allowed.

Reasons for Allowance

- 2. The following is a statement of reasons for the indication of allowable subject matter: The semiconductor device as recited in the claims of the instant invention fail to be taught by the prior art cited of interest. Kudo show debug device but fails to teach the specific characteristic of the structure recited in the claims of the instant invention: a semiconductor device having a CPU core, a first debug circuit section and a memory section integrated in a single semiconductor chip and a second semiconductor chip having a second debug circuit retained over the principal surface of the first semiconductor device. In Kudo's invention the memory section, core CPU and the debug circuit section are integrated in a single target board an not in a single semiconductor chip whereas the second debugger is located in an independent computer system.
- 3. Any comments considered necessary by applicant must be submitted no later than the payment of the issue fee and, to avoid processing delays, should preferably accompany the issue fee. Such submissions should be clearly labeled "Comments on Statement of Reasons for Allowance."

Application/Control Number: 10/621,654

Art Unit: 2826

Conclusion

Page 3

4. The prior art made of record and not relied upon is considered pertinent to

applicant's disclosure. Edwards (US 6,526,501) and Jones (US 6,697,931) show

integrated circuit devices similar to the instant invention.

5. Any inquiry concerning this communication or earlier communications from the

examiner should be directed to Leonardo Andújar whose telephone number is 571-272-

1912. The examiner can normally be reached on Mon through Thu from 9:00 AM to

7:30 PM EST.

6. If attempts to reach the examiner by telephone are unsuccessful, the examiner's

supervisor, Nathan J Flynn can be reached on 571-272-1915. The fax phone number

for the organization where this application or proceeding is assigned is 703-872-9306.

7. Information regarding the status of an application may be obtained from the

Status information for Patent Application Information Retrieval (PAIR) system.

published applications may be obtained from either Private PAIR or Public PAIR.

Status information for unpublished applications is available through Private PAIR only.

For more information about the PAIR system, see http://pair-direct.uspto.gov. Should

you have questions on access to the Private PAIR system, contact the Electronic

Business Center (EBC) at 866-217-9197 (toll-free).

Leonardo Andújar

Patent Examiner Art Unit 2826

02/05/2005